

**DESCRIPTION**

M54513P and M54513FP are eight-circuit transistor arrays. The circuits are made of NPN transistors. Both the semiconductor integrated circuits perform high-current driving with extremely low input-current supply.

**FEATURES**

- High breakdown voltage ( $BV_{CEO} \geq 40V$ )
- Synchronizing current ( $I_c(\max) = 50mA$ )
- Wide operating temperature range ( $T_a = -20$  to  $+75^{\circ}C$ )

**APPLICATION**

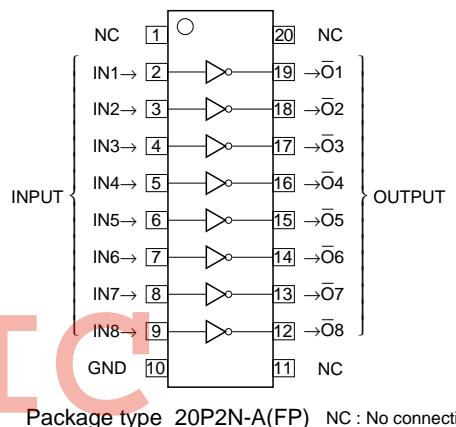
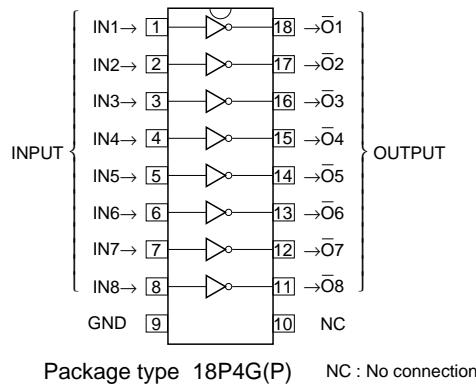
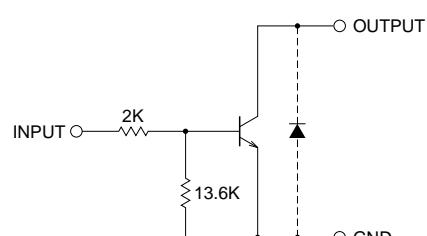
Driving of digit drives of indication elements (LEDs and lamps) with small signals

**FUNCTION**

The M54513P and M54513FP each have eight circuits consisting of NPN transistors. These ICs have resistance of 2 k $\Omega$  at inputs and of 13.6 k $\Omega$  between the base and emitter. The GND is used in common in each circuit.

The transistors allow synchronous flow of 50mA collector current. A maximum of 40V voltage can be applied between the collector and emitter.

The M54513FP is enclosed in a molded small flat package, enabling space-saving design.

**PIN CONFIGURATION****CIRCUIT DIAGRAM**

The eight circuits share the GND.

The diode, indicated with the dotted line, is parasitic, and cannot be used.

Unit :  $\Omega$

## 8-UNIT 50mA TRANSISTOR ARRAY

ABSOLUTE MAXIMUM RATINGS (Unless otherwise noted,  $T_a = -20 \sim +75^\circ\text{C}$ )

Symbol	Parameter	Conditions	Ratings	Unit
$V_{CEO}$	Collector-emitter voltage	Output, H	-0.5 ~ +40	V
$I_c$	Collector current	Current per circuit output, L	50	mA
$V_I$	Input voltage		-0.5 ~ +10	V
$P_d$	Power dissipation	$T_a = 25^\circ\text{C}$ , when mounted on board	1.79(P)/1.10(FP)	W
$T_{opr}$	Operating temperature		-20 ~ +75	$^\circ\text{C}$
$T_{stg}$	Storage temperature		-55 ~ +125	$^\circ\text{C}$

RECOMMENDED OPERATING CONDITIONS (Unless otherwise noted,  $T_a = -20 \sim +75^\circ\text{C}$ )

Symbol	Parameter	Limits			Unit
		min	typ	max	
$V_o$	Output voltage	0	—	40	V
$I_c$	Collector current	0	—	30	mA
$V_{IH}$	"H" input voltage	2	—	8	V
$V_{IL}$	"L" input voltage	0	—	0.2	V

ELECTRICAL CHARACTERISTICS (Unless otherwise noted,  $T_a = -20 \sim +75^\circ\text{C}$ )

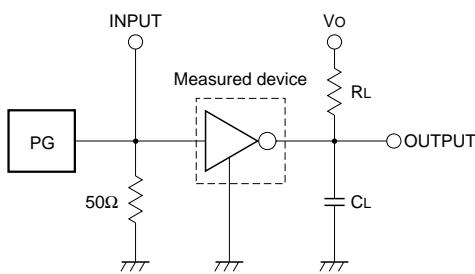
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ*	max	
$V_{(BR)CEO}$	Collector-emitter breakdown voltage	$I_{CEO} = 100\mu\text{A}$	40	—	—	V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_I = 2\text{V}$ , $I_c = 10\text{mA}$ $V_I = 2.5\text{V}$ , $I_c = 30\text{mA}$	—	25	100	mV
$I_I$	Input current	$V_I = 2.5\text{V}$	—	70	170	
$hFE$	DC amplification factor	$V_{CE} = 4\text{V}$ , $I_c = 30\text{mA}$ , $T_a = 25^\circ\text{C}$	80	200	—	

\* : The typical values are those measured under ambient temperature ( $T_a$ ) of  $25^\circ\text{C}$ . There is no guarantee that these values are obtained under any conditions.

SWITCHING CHARACTERISTICS (Unless otherwise noted,  $T_a = 25^\circ\text{C}$ )

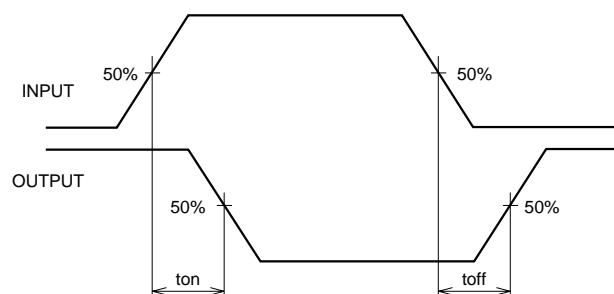
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
$t_{on}$	Turn-on time	$CL = 15\text{pF}$ (note 1)	—	65	—	ns
$t_{off}$	Turn-off time		—	1200	—	ns

## NOTE 1 TEST CIRCUIT



- (1) Pulse generator (PG) characteristics : PRR = 1kHz,  $t_w = 10\mu\text{s}$ ,  $t_r = 6\text{ns}$ ,  $t_f = 6\text{ns}$ ,  $Z_0 = 50\Omega$ ,  $V_P = 2.5\text{Vp-p}$
- (2) Output conditions :  $RL = 300\Omega$ ,  $VO = 10\text{V}$
- (3) Electrostatic capacity  $CL$  includes floating capacitance at connections and input capacitance at probes

## TIMING DIAGRAM



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**TYPICAL CHARACTERISTICS**