

M54564P/FP

8-UNIT 500mA SOURCE TYPE DARLINGTON TRANSISTOR ARRAY

DESCRIPTION

M54564P and M54564FP are eight-circuit output-sourcing Darlington transistor arrays. The circuits are made of PNP and NPN transistors. Both the semiconductor integrated circuits perform high-current driving with extremely low input-current supply.

FEATURES

- High breakdown voltage ($BV_{CEO} \geq 50V$)
- High-current driving ($I_{O(max)} = -500mA$)
- With output pulldown resistance (Driving available with fluorescent display tube)
- Driving available with PMOS IC output or with TTL output
- Wide operating temperature range ($T_a = -20$ to $+75^{\circ}C$)
- Output current-sourcing type

APPLICATION

Drives of relays, printers, LEDs, fluorescent display tubes and lamps, and interfaces between MOS-bipolar logic systems and relays, solenoids, or small motors

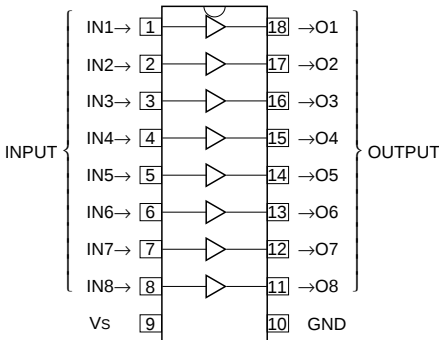
FUNCTION

The M54564P and M54564FP each have eight circuits, which are made of input inverters and current-sourcing outputs. The outputs are made of PNP transistors and NPN Darlington transistors. The PNP transistor base current is constant. Resistance of $50k\Omega$ is connected between each output pin and GND, making these ICs suitable for fluorescent display tubes. V_s and GND are used commonly among the eight circuits.

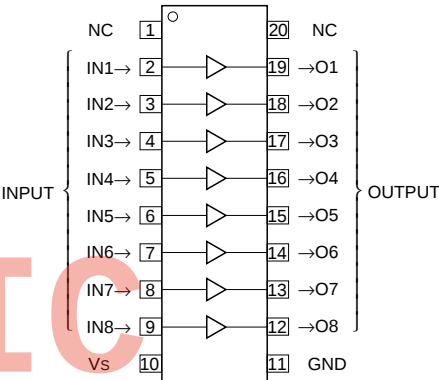
Output current is 500mA maximum. Supply voltage V_s is 50V maximum.

The M54564FP is enclosed in a molded small flat package, enabling space-saving design.

PIN CONFIGURATION



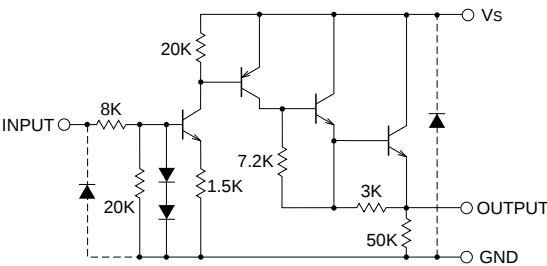
Package type 18P4G(P)



Package type 20P2N-A(FP)

NC : No connection

CIRCUIT DIAGRAM



The eight circuits share the V_s and GND.

The diode, indicated with the dotted line, is parasitic, and cannot be used.

Unit : Ω

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ABSOLUTE MAXIMUM RATINGS
(Unless otherwise noted, Ta = -20 ~ +75°C)

Symbol	Parameter	Conditions	Ratings	Unit
VCEO	Collector-emitter voltage	Output, L	-0.5 ~ +50	V
Vs	Supply voltage		50	V
Vi	Input voltage		-0.5 ~ +30	V
Io	Output current	Current per circuit output, H	-500	mA
Pd	Power dissipation	Ta = 25°C, when mounted on board	1.79(P)/1.10(FP)	W
Topr	Operating temperature		-20 ~ +75	°C
Tstg	Storage temperature		-55 ~ +125	°C

RECOMMENDED OPERATING CONDITIONS
(Unless otherwise noted, Ta = -20 ~ +75°C)

Symbol	Parameter		Limits			Unit
			min	typ	max	
Vs	Supply voltage		0	—	50	V
Io	Output current (Current per 1 circuit when 8 circuits are coming on simultaneously)	Duty Cycle P : no more than 8% FP : no more than 5%	0	—	−350	mA
		Duty Cycle P : no more than 55% FP : no more than 30%	0	—	−100	
VIH	“H” input voltage		2.4	—	25	V
VIL	“L” input voltage		0	—	0.2	V

ELECTRICAL CHARACTERISTICS
(Unless otherwise noted, Ta = -20 ~ +75°C)

Symbol	Parameter	Test conditions	Limits			Unit
			min	typ*	max	
IS (leak)	Supply leak current	Vs = 50V, Vi = 0.2V	—	—	100	μA
VCE (sat)	Collector-emitter saturation voltage	Vs = 10V, Vi = 2.4V, Io = -350mA	—	1.6	2.4	V
		Vs = 10V, Vi = 2.4V, Io = -100mA	—	1.45	2.0	
Ii	Input current	Vi = 5V, Vs = 10V	—	0.4	0.7	mA
		Vi = 25V, Vs = 30V	—	2.9	6.5	
IS	Supply current	Vs = 50V, Vi = 5V	—	—	5.0	mA

* : The typical values are those measured under ambient temperature (Ta) of 25°C. There is no guarantee that these values are obtained under any conditions.

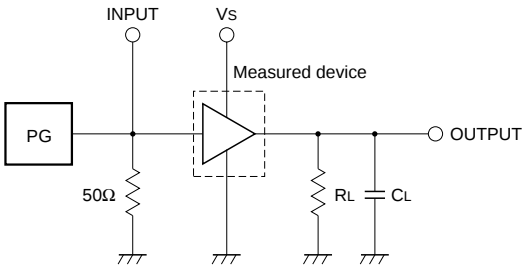
SWITCHING CHARACTERISTICS
(Unless otherwise noted, Ta = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
ton	Turn-on time	CL = 15pF (note 1)	—	185	—	ns
toff	Turn-off time		—	4300	—	ns

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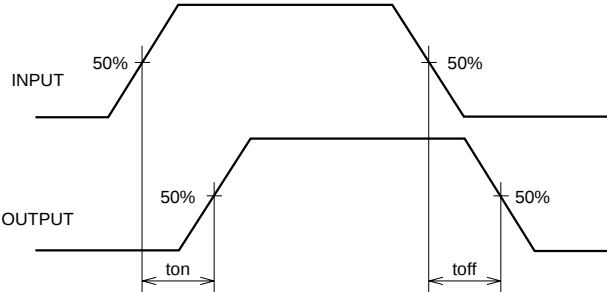
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NOTE 1 TEST CIRCUIT

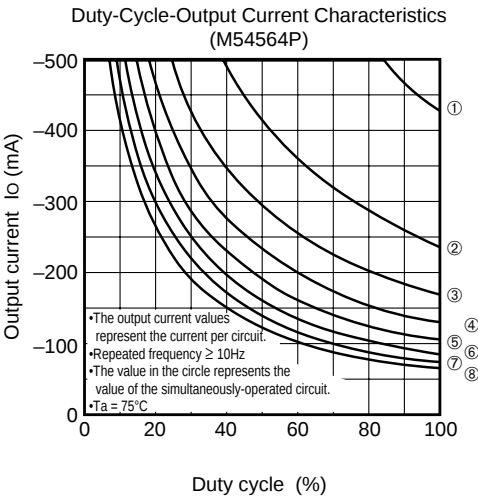
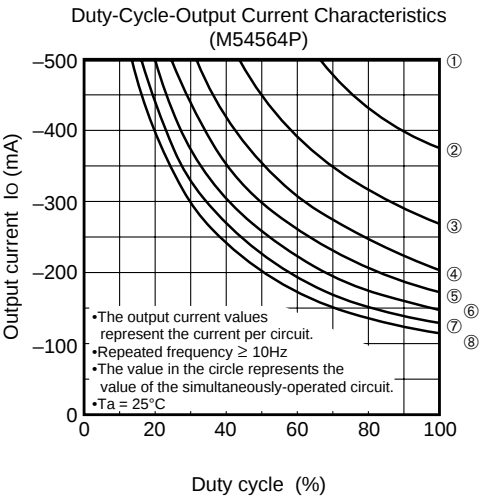
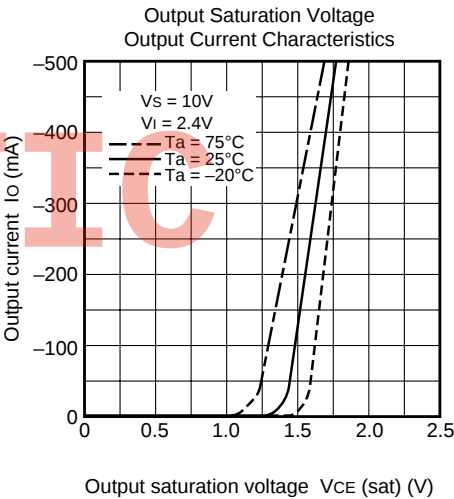
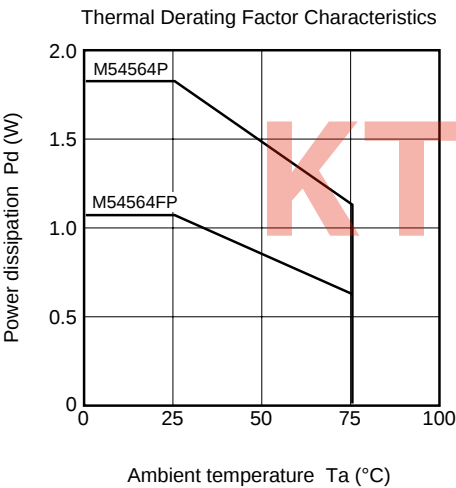


- (1) Pulse generator (PG) characteristics : PRR = 1kHz,
tw = 10μs, tr = 6ns, tf = 6ns, Zo = 50Ω
Vi = 0 to 2.4V
- (2) Input-output conditions : RL = 30Ω, Vs = 10V
- (3) Electrostatic capacity CL includes floating capacitance at connections and input capacitance at probes

TIMING DIAGRAM



TYPICAL CHARACTERISTICS



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