

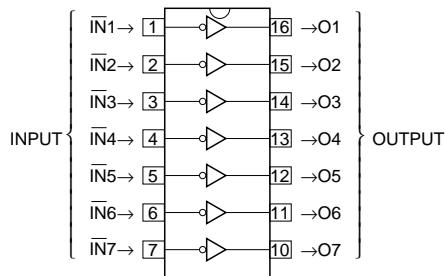
7-UNIT 150mA SOURCE TYPE DARLINGTON TRANSISTOR ARRAY

**DESCRIPTION**

M54580P and M54580FP are seven-circuit output-sourcing Darlington transistor arrays. The circuits are made of PNP and NPN transistors. Both the semiconductor integrated circuits perform high-current driving with extremely low input-current supply.

**FEATURES**

- High breakdown voltage ( $BV_{CEO} \geq 50V$ )
- High-current driving ( $I_o(\max) = -150mA$ )
- Active L-level input
- With input diodes
- Wide operating temperature range ( $T_a = -20$  to  $+75^{\circ}C$ )

**PIN CONFIGURATION**

16P4(P)  
Package type 16P2N-A(FP)

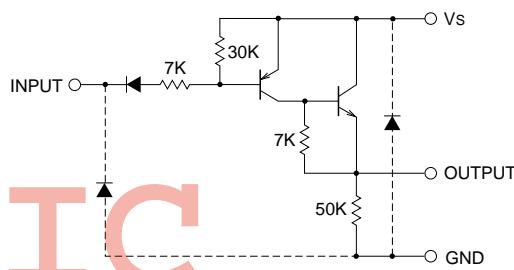
**APPLICATION**

Drives of relays, printers and indication elements such as LEDs, fluorescent display tubes and lamps, and interfaces between MOS-bipolar logic systems and relays, solenoids, or small motors

**FUNCTION**

The M54580P and M54580FP each have seven circuits, which are made of output current-sourcing Darlington transistors consisting of PNP and NPN transistors. Each PNP transistor has a diode and resistance of  $7k\Omega$  between the base and input pin. Its emitter and NPN transistor collectors are connected to the Vs pin (pin 9). Resistance of  $50k\Omega$  is connected between each output pin and GND pin (pin 8). Output current is 150mA maximum. Supply voltage Vs is 50V maximum.

The M54580FP is enclosed in a molded small flat package, enabling space-saving design.

**CIRCUIT DIAGRAM**

The seven circuits share the Vs and GND.

The diode, indicated with the dotted line, is parasitic, and cannot be used.

Unit :  $\Omega$

**ABSOLUTE MAXIMUM RATINGS** (Unless otherwise noted,  $T_a = -20$  ~  $+75^{\circ}C$ )

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>CEO</sub>	Collector-emitter voltage	Output, L	-0.5 ~ +50	V
V <sub>s</sub>	Supply voltage		50	V
V <sub>i</sub>	Input voltage		-0.5 ~ V <sub>s</sub>	V
I <sub>o</sub>	Output current	Current per circuit output, H	-150	mA
P <sub>d</sub>	Power dissipation	T <sub>a</sub> = 25°C, when mounted on board	1.47(P)/1.00(FP)	W
T <sub>opr</sub>	Operating temperature		-20 ~ +75	°C
T <sub>stg</sub>	Storage temperature		-55 ~ +125	°C

Aug. 1999

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RECOMMENDED OPERATING CONDITIONS (unless otherwise noted,  $T_a = -20 \sim +75^\circ\text{C}$ )

Symbol	Parameter	Limits			Unit	
		min	typ	max		
V <sub>s</sub>	Supply voltage	4	—	50	V	
I <sub>o</sub>	Output current (Current per 1 circuit when 7 circuits are coming on simultaneously)	Duty Cycle P : no more than 85% FP : no more than 50%	0	—	-100	mA
		Duty Cycle P : no more than 100% FP : no more than 100%	0	—	-50	
V <sub>ih</sub>	"H" input voltage	V <sub>s</sub> -0.4	—	V <sub>s</sub>	V	
V <sub>il</sub>	"L" input voltage	0	—	V <sub>s</sub> -3.2	V	

ELECTRICAL CHARACTERISTICS (Unless otherwise noted,  $T_a = -20 \sim +75^\circ\text{C}$ )

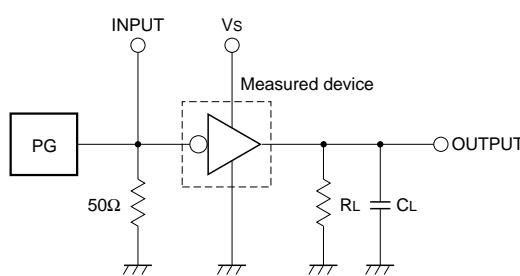
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ*	max	
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	I <sub>CEO</sub> = 100μA	50	—	—	V
V <sub>ce(sat)</sub>	Collector-emitter saturation voltage	VI = V <sub>s</sub> -3.2V, I <sub>o</sub> = -100mA	—	0.9	1.5	V
		VI = V <sub>s</sub> -3.2V, I <sub>o</sub> = -50mA	—	0.8	1.2	
I <sub>i</sub>	Input current	VI = V <sub>s</sub> -3.5V	—	-0.3	-0.6	mA
		VI = V <sub>s</sub> -6V	—	-0.65	-0.95	
I <sub>r</sub>	Clamping diode reverse current	VI = 40V	—	—	100	μA
h <sub>FE</sub>	DC amplification factor	V <sub>ce</sub> = 4V, V <sub>s</sub> = 10V, I <sub>c</sub> = -100mA, T <sub>a</sub> = 25°C	800	3000	—	—

\* : The typical values are those measured under ambient temperature (T<sub>a</sub>) of 25°C. There is no guarantee that these values are obtained under any conditions.

SWITCHING CHARACTERISTICS (Unless otherwise noted,  $T_a = 25^\circ\text{C}$ )

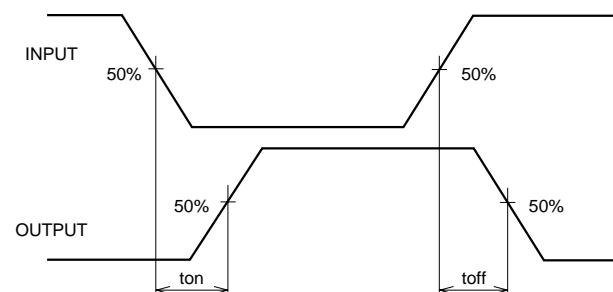
Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
t <sub>on</sub>	Turn-on time	CL = 15pF (note 1)	—	200	—	ns
t <sub>off</sub>	Turn-off time		—	7500	—	ns

## NOTE 1 TEST CIRCUIT



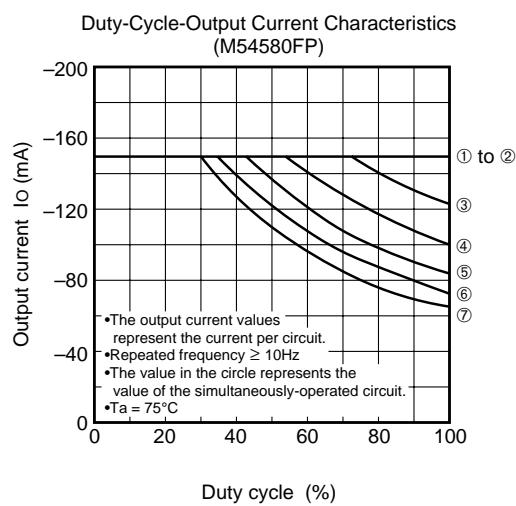
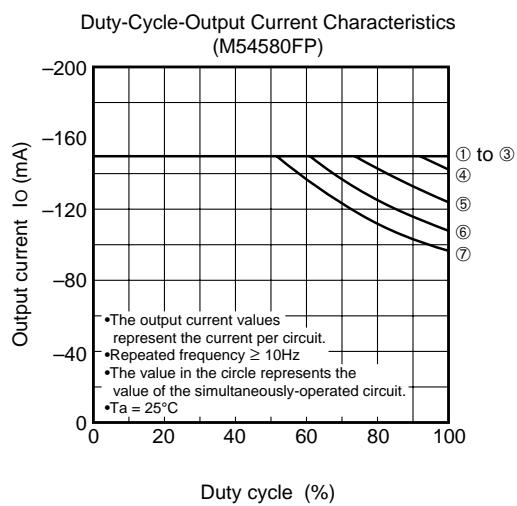
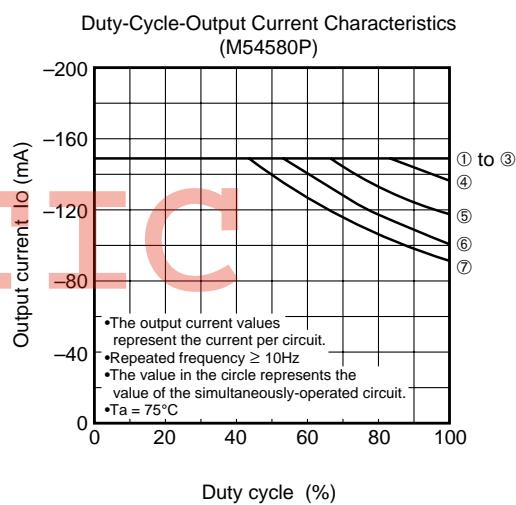
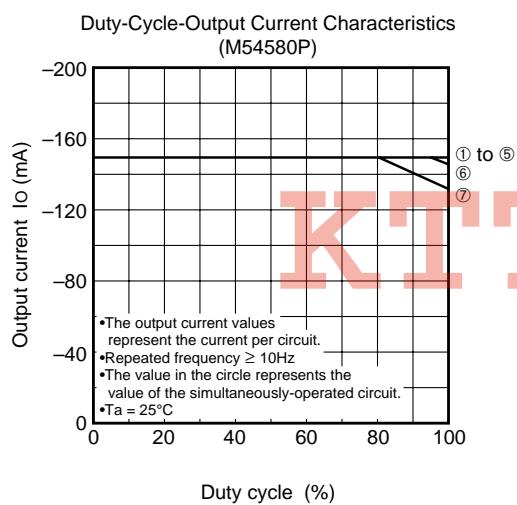
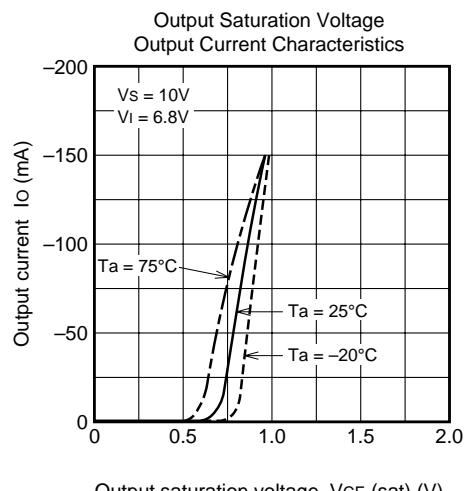
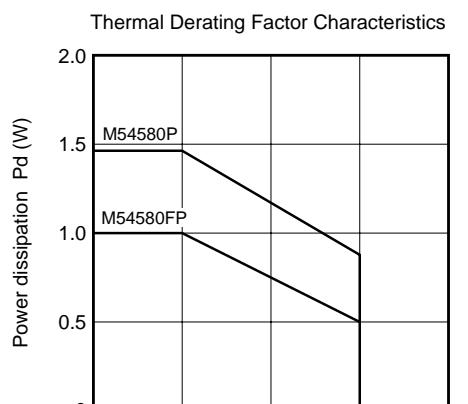
- (1) Pulse generator (PG) characteristics : PRR = 1kHz,  
tw = 10μs, tr = 6ns, tf = 6ns, Zo = 50Ω  
VI = 0.8 to 4V
- (2) Input-output conditions : RL = 40Ω, Vs = 4V
- (3) Electrostatic capacity CL includes floating capacitance at connections and input capacitance at probes

## TIMING DIAGRAM



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## TYPICAL CHARACTERISTICS



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