

M63802P/FP/GP/KP

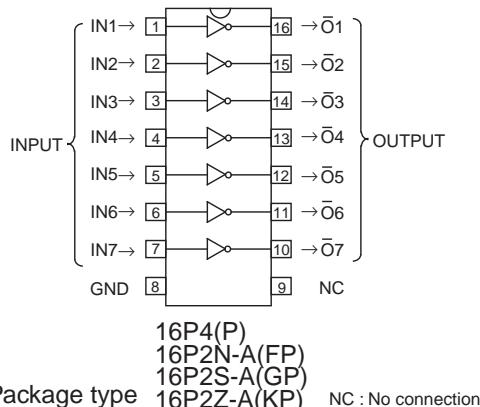
7-UNIT 300mA TRANSISTOR ARRAY

DESCRIPTION

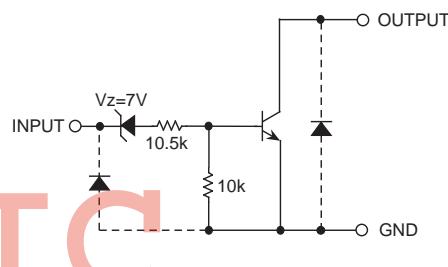
M63802P, M63802FP, M63802GP and M63802KP are seven-circuit Single transistor arrays. The circuits are made of NPN transistors. Both the semiconductor integrated circuits perform high-current driving with extremely low input-current supply.

FEATURES

- Four package configurations (P, FP, GP and KP)
- Medium breakdown voltage ($BV_{CEO} \geq 35V$)
- Synchronizing current ($I_C(max) = 300mA$)
- With zener diodes
- Low output saturation voltage
- Wide operating temperature range ($T_a = -40$ to $+85^{\circ}C$)

PIN CONFIGURATION**APPLICATION**

Driving of digit drives of indication elements (LEDs and lamps) with small signals

CIRCUIT DIAGRAM

The seven circuits share the GND.

The diode, indicated with the dotted line, is parasitic, and cannot be used.

Unit: Ω

FUNCTION

The M63802P, M63802FP, M63802GP and M63802KP each have seven circuits consisting of NPN transistor. The transistor emitters are all connected to the GND pin (pin 8). The transistors allow synchronous flow of 300mA collector current. A maximum of 35V voltage can be applied between the collector and emitter.

ABSOLUTE MAXIMUM RATINGS (Unless otherwise noted, $T_a = -40$ ~ $+85^{\circ}C$)

Symbol	Parameter	Conditions	Ratings	Unit
V _{CEO}	Collector-emitter voltage	Output, H	-0.5 ~ +35	V
I _C	Collector current	Current per circuit output, L	300	mA
V _I	Input voltage		-0.5 ~ +35	V
P _d	Power dissipation	Ta = 25°C, when mounted on board	M63802P	1.47
			M63802FP	1.00
			M63802GP	0.80
			M63802KP	0.78
T _{op}	Operating temperature		-40 ~ +85	°C
T _{stg}	Storage temperature		-55 ~ +125	°C

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RECOMMENDED OPERATING CONDITIONS (Unless otherwise noted, $T_a = -40 \sim +85^\circ\text{C}$)

Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
Vo	Output voltage		0	—	35	V
Ic	Collector current (Current per 1 circuit when 7 circuits are coming on simulta- neously)	M63802P	Duty Cycle no more than 45%	0	—	250
			Duty Cycle no more than 100%	0	—	160
		M63802FP	Duty Cycle no more than 30%	0	—	250
			Duty Cycle no more than 100%	0	—	130
		M63802GP	Duty Cycle no more than 24%	0	—	250
			Duty Cycle no more than 100%	0	—	120
		M63802KP	Duty Cycle no more than 24%	0	—	250
			Duty Cycle no more than 100%	0	—	120
VIN	Input voltage		0	—	30	V

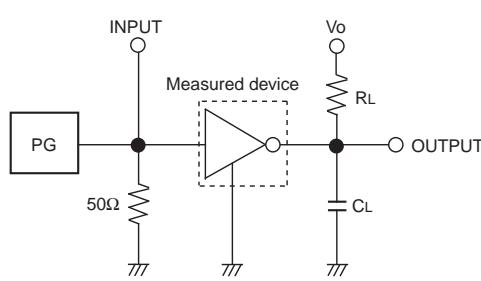
ELECTRICAL CHARACTERISTICS (Unless otherwise noted, $T_a = 25^\circ\text{C}$)

Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
V (BR) CEO	Collector-emitter breakdown voltage	$I_{CEO} = 10\mu\text{A}$	35	—	—	V
VCE(sat)	Collector-emitter saturation voltage	$I_{IN} = 1\text{mA}, I_C = 10\text{mA}$	—	—	0.2	V
		$I_{IN} = 2\text{mA}, I_C = 150\text{mA}$	—	—	0.8	
VIN(on)	"On" input voltage	$I_{IN} = 1\text{mA}, I_C = 10\text{mA}$	13	19	23	V
hFE	DC amplification factor	$V_{CE} = 10\text{V}, I_C = 10\text{mA}$	50	—	—	—

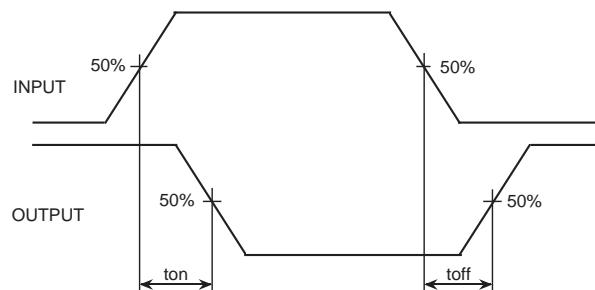
SWITCHING CHARACTERISTICS (Unless otherwise noted, $T_a = 25^\circ\text{C}$)

Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
ton	Turn-on time		—	140	—	ns
toff	Turn-off time	$CL = 15\text{pF}$ (note 1)	—	240	—	ns

NOTE 1 TEST CIRCUIT



TIMING DIAGRAM



- (1)Pulse generator (PG) characteristics : PRR = 1kHz,
 $t_w = 10\mu\text{s}$, $t_r = 6\text{ns}$, $t_f = 6\text{ns}$, $Z_0 = 50\Omega$, $V_{IH} = 18\text{V}$
(2)Input-output conditions : $RL = 220\Omega$, $Vo = 35\text{V}$
(3)Electrostatic capacity CL includes floating capacitance at connections and input capacitance at probes

TYPICAL CHARACTERISTICS

