



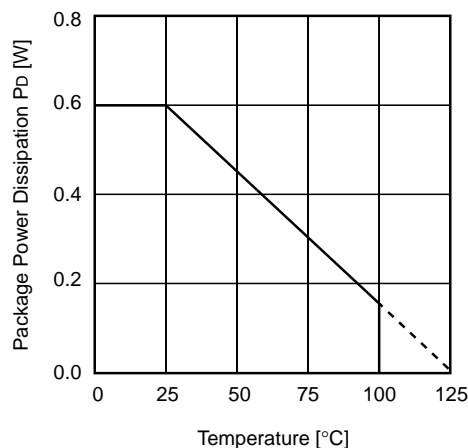
**ABSOLUTE MAXIMUM RATINGS**

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>B</sub>	High Side Floating Supply Absolute Voltage		-0.5 ~ 624	V
V <sub>S</sub>	High Side Floating Supply Offset Voltage		V <sub>B</sub> -24 ~ V <sub>B</sub> +0.5	V
V <sub>BS</sub>	High Side Floating Supply Voltage	V <sub>BS</sub> = V <sub>B</sub> -V <sub>S</sub>	-0.5 ~ 24	V
V <sub>OUT</sub>	High Side Output Voltage		V <sub>S</sub> -0.5 ~ V <sub>B</sub> +0.5	V
V <sub>CC</sub>	Low Side Fixed Supply Voltage		-0.5 ~ 24	V
V <sub>IN</sub>	Logic Input Voltage		-0.5 ~ 5.5	V
dV <sub>S</sub> /dt	Allowable Offset Supply Voltage Transient		±50	V/ns
P <sub>D</sub>	Package Power Dissipation	T <sub>a</sub> = 25°C, On Board	0.60	W
K <sub>θ</sub>	Linear Derating Factor	T <sub>a</sub> > 25°C, On Board	-6.0	mW/°C
R <sub>th(j-c)</sub>	Junction-Case Thermal Resistance		50	°C/W
T <sub>j</sub>	Junction Temperature		-20 ~ 125	°C
T <sub>opr</sub>	Operation Temperature		-20 ~ 100	°C
T <sub>stg</sub>	Storage Temperature		-40 ~ 125	°C

**RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V <sub>B</sub>	High Side Floating Supply Absolute Voltage		V <sub>S</sub> +13.5	—	V <sub>S</sub> +20	V
V <sub>S</sub>	High Side Floating Supply Offset Voltage	V <sub>B</sub> > 13.5V	-5	—	500	V
V <sub>BS</sub>	High Side Floating Supply Voltage	V <sub>BS</sub> = V <sub>B</sub> -V <sub>S</sub>	13.5	—	20	V
V <sub>CC</sub>	Low Side Fixed Supply Voltage		13.5	—	20	V
V <sub>IN</sub>	Logic Input Voltage		0	—	5	V

**THERMAL DERATING FACTOR CHARACTERISTIC**



**ELECTRICAL CHARACTERISTICS (Ta=25°C, Vcc=Vbs (=Vb-Vs)=15V, unless otherwise specified)**

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.*	Max.	
IFS	Floating Supply Leakage Current	Vb=Vs=600V	—	—	1.0	μA
IbS	Vbs Standby Current		0.25	0.50	0.75	mA
Icc	Vcc Standby Current		0.50	0.75	1.00	mA
VOH	High Level Output Voltage	Io=0A	14.9	—	—	V
VOL	Low Level Output Voltage	Io=0A	—	—	0.1	V
VIH	High Level Input Threshold Voltage		2.5	3.0	4.0	V
VIL	Low Level Input Threshold Voltage		0.8	1.5	2.0	V
IiH	High Level Input Bias Current	VIN=5V	-50	-20	—	μA
IiL	Low Level Input Bias Current	VIN=0V	-200	-100	—	μA
VBSuvr	Vbs Supply UV Reset Voltage		10.5	11.5	12.5	V
VBSuvh	Vbs Supply UV Hysteresis Voltage		0.2	0.5	0.8	V
tVBSuv	Vbs Supply UV Filter Time		—	5	—	μs
IOH	Output High Level Short Circuit Pulsed Current	Vo=0V, VIN=0V, PW<10μs	—	-125	—	mA
IOL1	Output Low Level Short Circuit Pulsed Current	Vo=1V, VIN=5V, PW<10μs	—	40	—	mA
IOL2	Output Low Level Short Circuit Pulsed Current	Vo=15V, VIN=5V, PW<10μs	—	150	—	mA
ROH	Output High Level On Resistance	Io=-100mA, ROH=(VOH-Vo)/Io	—	120	160	Ω
ROL1	Output Low Level On Resistance1	Vo=1V, ROL1=Vo/Io	—	50	60	Ω
ROL2	Output Low Level On Resistance2	Vo=5V, ROL2=Vo/Io	—	100	130	Ω
tdLH	High Side Turn-On Propagation Delay	CL=1000pF between OUT – Vs	100	—	500	ns
tdHL	High Side Turn-Off Propagation Delay	CL=1000pF between OUT – Vs	100	—	500	ns
tr	High Side Turn-On Rise Time	CL=1000pF between OUT – Vs	—	220	—	ns
tf	High Side Turn-Off Fall Time	CL=1000pF between OUT – Vs	—	110	—	ns
VOth	ROL1/ROL2 Switching Output Voltage		1.5	2.5	4.0	V

\* Typ. is not specified.

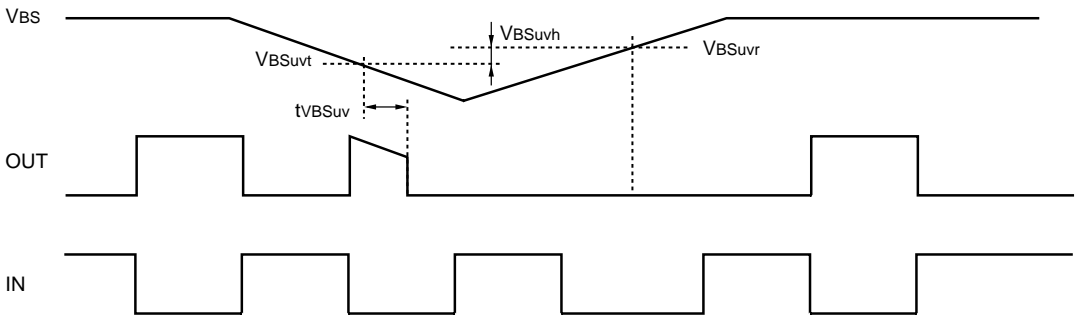
**TIMING DIAGRAM**

1. Input/Output Timing Diagram

When input signal is "L", then output signal "H".

2. VBS Supply Under Voltage Lockout Timing Diagram

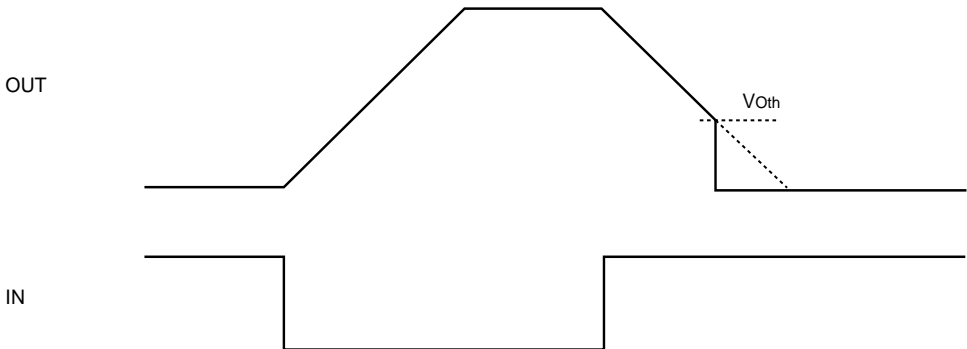
When VBS Supply Voltage keeps lower UV Trip Voltage ( $VBS_{uvt} = VBS_{uvr} - VBS_{uvh}$ ) for VBS Supply UV Filter Time, output signal becomes "L". And then, VBS Supply Voltage is higher UV Reset Voltage, output signal keeps "L" until next input signal is "L".



3. Allowable supply voltage transient

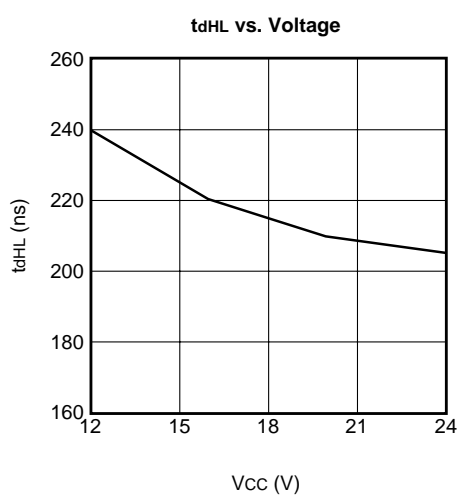
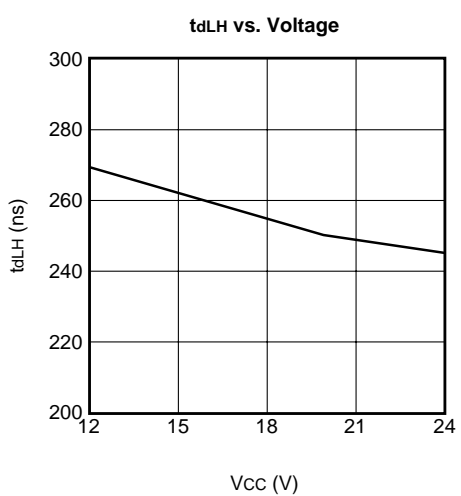
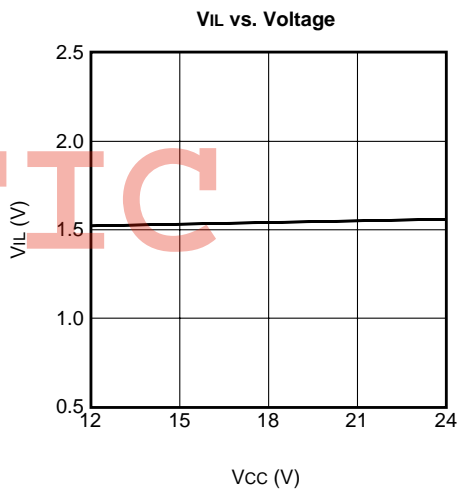
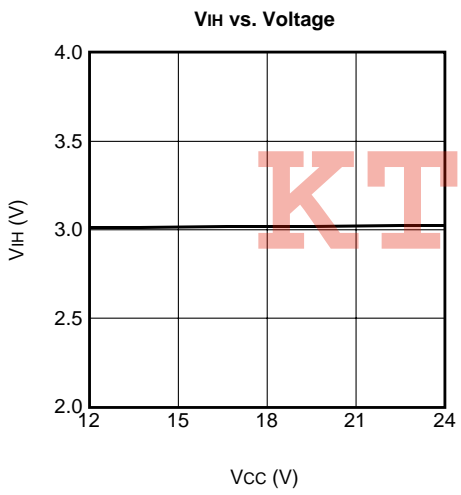
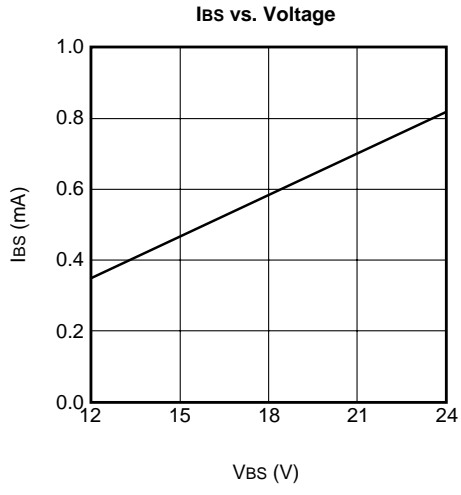
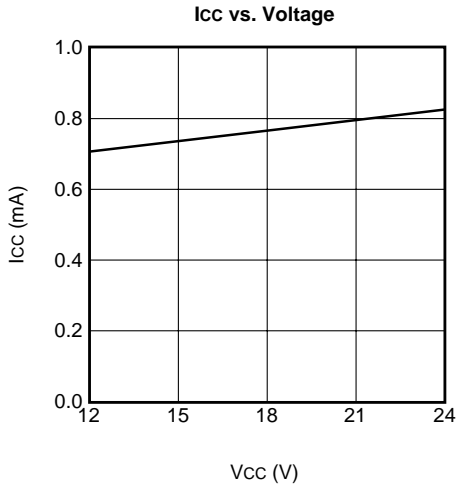
Firstly, supply VCC with voltage. Secondly, supply VBS with voltage. In the case of shutting off supply voltage, shut off VBS Supply Voltage firstly. Secondly, shut off VCC Supply Voltage. In case VBS or VCC is started too fast, output signal may be "H".

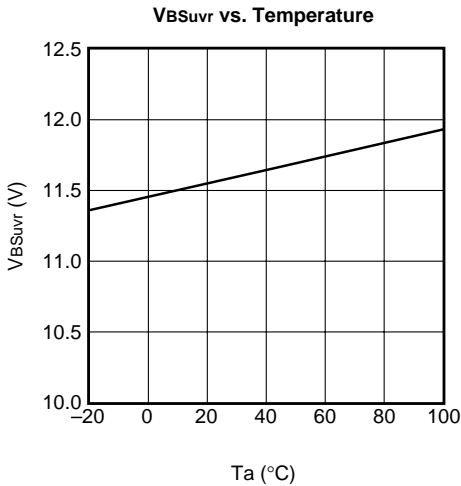
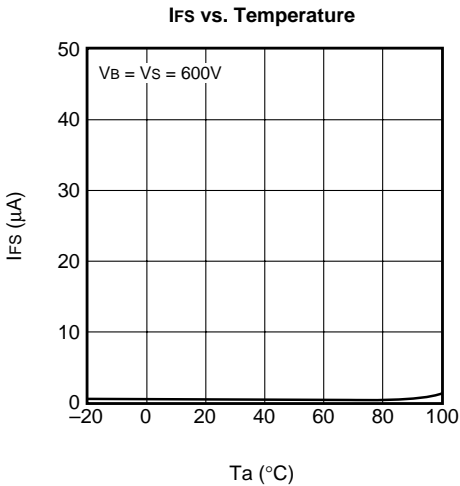
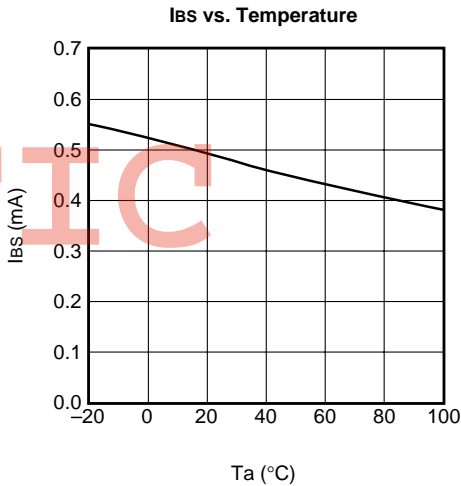
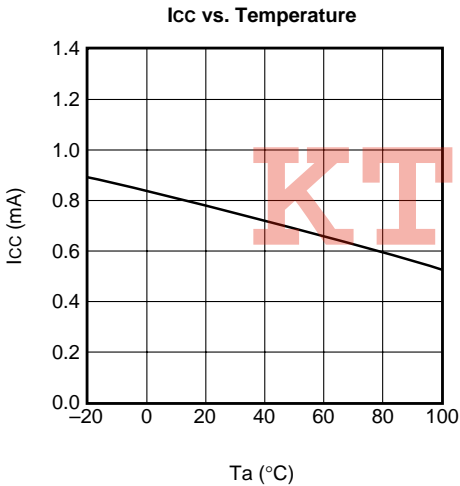
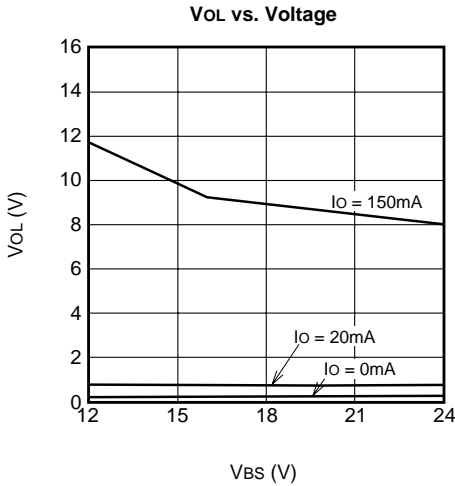
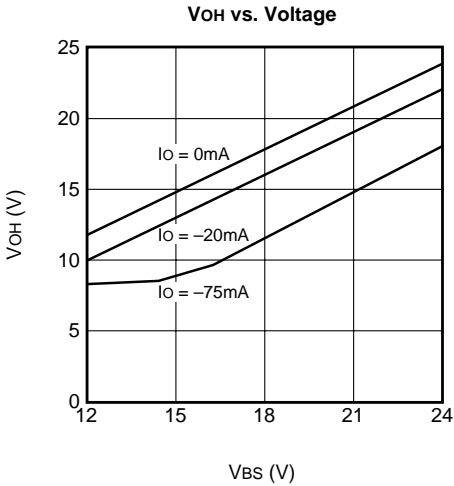
4. ROL1/ROL2 Switching Output Voltage  $V_{Oth}$



As shown by the solid line of the timing chart, the output on-resistance drops at 'VOth' level when the output is in the 'L' state (output level falls). Below the 'VOth' level, the output level falls more steeply.

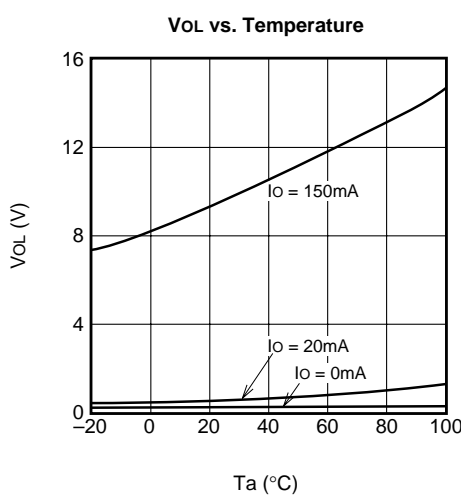
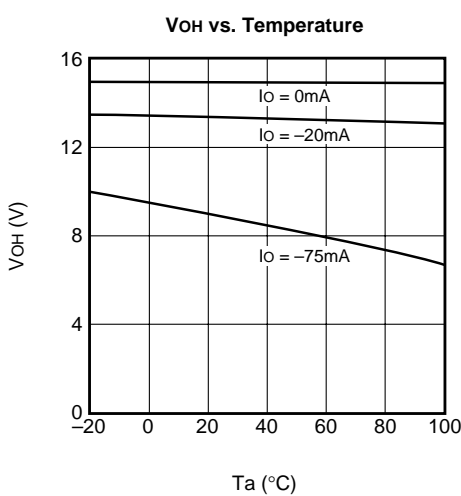
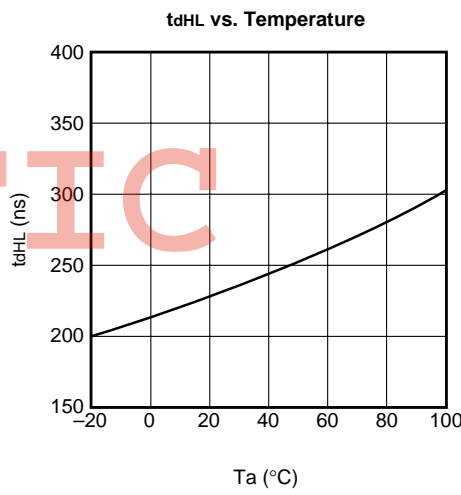
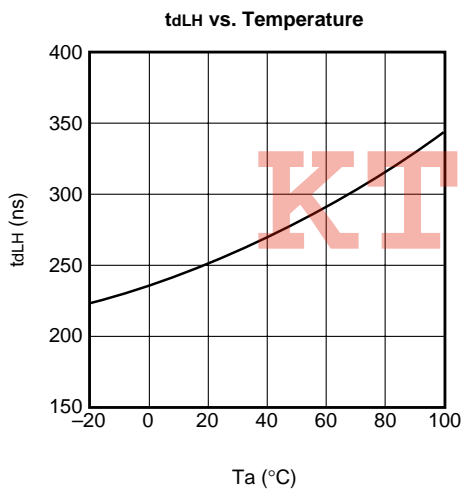
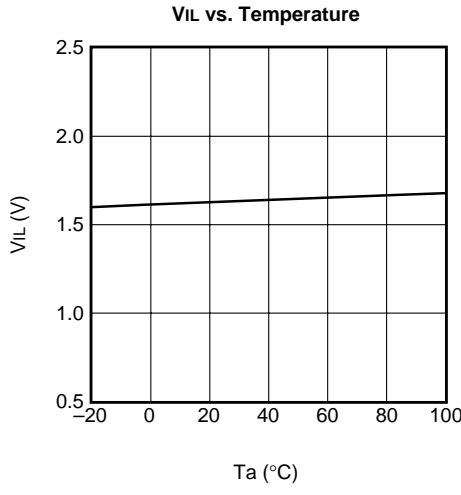
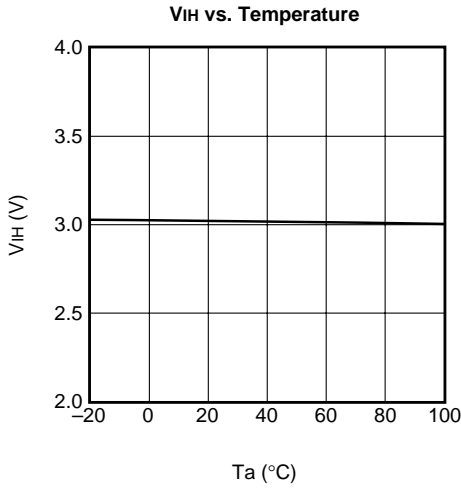
PERFORMANCE CURVES

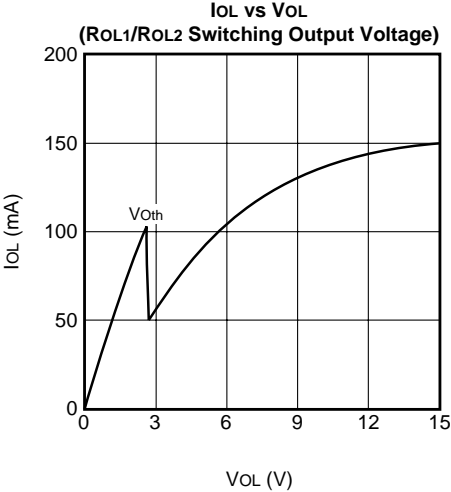




**M81705FP**

**HIGH VOLTAGE HIGH SIDE DRIVER**





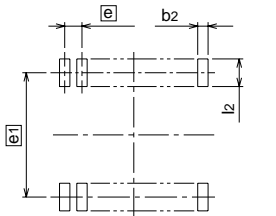
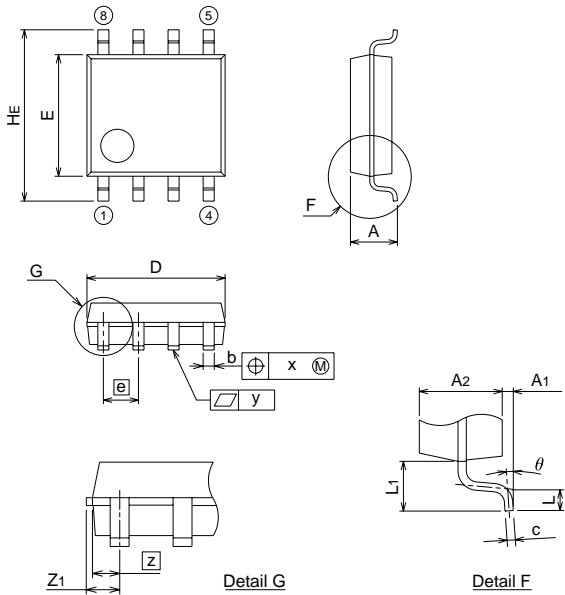
PACKAGE OUTLINE

KTTIC

8P2S-A

Plastic 8pin 225mil SOP

EIAJ Package Code	JEDEC Code	Weight(g)	Lead Material
SOP8-P-225-1.27	-	0.07	Cu Alloy



Recommended Mount Pad

Symbol	Dimension in Millimeters		
	Min	Nom	Max
A	-	-	1.9
A1	0.05	-	-
A2	-	1.5	-
b	0.35	0.4	0.5
c	0.13	0.15	0.2
D	4.8	5.0	5.2
E	4.2	4.4	4.6
e	-	1.27	-
HE	5.9	6.2	6.5
L	0.2	0.4	0.6
L1	-	0.9	-
Z	-	0.595	-
Z1	-	-	0.745
x	-	-	0.25
y	-	-	0.1
theta	0°	-	10°
b2	-	0.76	-
e1	-	5.72	-
l2	1.27	-	-